



72nd
International
Electron
Devices
Meeting

Devices at the Heart of the Intelligence Revolution

2026 International
Electron Devices
Meeting

December 12-16, 2026

Hilton San Francisco Union Square
San Francisco, CA

IEDM is pleased to announce increased
technical focus in the area of:

MEMORY TECHNOLOGY (MT)

Topics

Papers are solicited in the following themes of interest:

- Advancements in embedded and stand-alone memories
- Emerging memories
- 3D memory technologies
- Memories (such as HBM) for AI or near memory computing applications
- In-Package Memory for PPA augmentation

New or trending areas include:

- Memories to break the memory wall
- Memory-enabled artificial intelligence applications
- Memory-Logic 3D stacking
- System-technology co-optimization
- Memory Pooling and communication
- New memory hierarchy

Paper Submission

Deadline: July 16th

Single submission of final, four-page paper



For More Information

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