

Innovative trends in device technology to enable the next computing revolution

Organizer: Anne Vandooren, IMEC

World experts will cover in depth the latest innovative trends in device technology. This short course will address the main challenges for the next computing era, covering a wide range of topics going from RF front-end & power devices to 3D technology and extremely scaled nanosheet devices.”

Anne Vandooren received her M.S. Degree in Electrical Engineer from the Université Catholique de Louvain, Belgium in 1996 and her PhD degree in Electrical Engineering from the University of California, Davis in 2000. She worked for Motorola (later spun off as Freescale Semiconductor Inc.) until 2007 in the APRDL department where she conducted advanced research on SOI-based fully depleted devices including planar single-gate and multi-gate transistors. She is currently a Member of Technical Staff with imec in Belgium where she has been involved in the design, characterization and integration of tunneling-based novel transistors and, more recently, on 3D sequential device stacking in the LOGIC program. She authored and co-authored more than 100 papers in technical journals and international conferences, as well as 10 patents.