

3D sequential integration: opportunities, breakthrough and challenges

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Biography: Claire Fenouillet-Beranger received the postgraduate diploma in microelectronics and PhD degree from the Institut National Polytechnique de Grenoble, France, in 1998 and 2001, respectively. In 1998, she joined LETI, Grenoble, where she carried out her PhD. work on the integration and characterization of SOI devices. From 2001 to 2013 she worked as a CEA/LETI assignee in advanced R&D STMicroelectronics center, Crolles, France on FDSOI (Fully-depleted SOI) technology platform development and characterization. Since 2013 she is senior scientist and works as the project leader of the low temperature MOSFETs development for 3D sequential integration. In parallel she was a scientific delegate in her LETI department.

In the frame of these activities, she received the Best Paper Awards: at ESSDERC 2009 in Athens, and the best student paper at IEEE VLSI-TSA 2010 in Taiwan. She served in the program committee of SISC conference (Semiconductor Interface specialists conference 2018-2019) and EUROSOCI conference in 2019.

She is the author and co-author of more than 190 publications in major conferences and journals and of more than 30 patents. She was the co-recipient of the Grand Prix du Général Ferrié in 2012 for her work on FDSOI.

Since January 2020, she is the LETI SiC pilote line project manager in the frame of the joint development program between SOITEC & AMAT.