



2019 IEDM Conference Theme
**Innovative Devices for an Era
of Connected Intelligence**



For More Information

IEDM Online: iee-iedm.org

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2019 IEEE International Electron Devices Meeting

December 7th – 11th, 2019

Hilton San Francisco Union Square
San Francisco, California

Focused Call for Papers

IEDM is pleased to announce increased technical focus in the area of:

Advanced Logic Technology (ALT)

Topics

Papers are solicited in the following themes of interest:

- CMOS platform technologies
- Logic device performance and circuit design challenges and solutions
- Advanced process integration schemes and scaling approaches
- Process module innovations and process control advancements
- Device technology co-optimization

New or trending areas include:

- SiGe/Ge channel, GAA nanowire and stacked nanosheet advanced device technologies
- Sequential monolithic 3D integration and heterogeneous chiplets
- BEOL compatible transistors

Streamlined Paper Submission

Submission deadline: July 26th
Single submission of final, four-page paper