

Variability and design for manufacturability

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- Long range, short range and statistical variability
- Time dependent variability
- Variability aware compact model
- Design-Technology Co-Optimisation

Asen Asenov (FIEEE, FRSE) is a founder and CEO of Gold Standard Simulations (GSS) Ltd. (www.goldstandardsimulations.com). GSS is the leader in predictive simulation of performance and of statistical variability in advanced CMOS, compact model extraction and generation technology and statistical circuit simulation. The GSS customers include foundries, IDMs, and fables companies. Asenov is also a Director of SureCore, Ltd, a low power SRAM design IP start-up company, SemiWise, Ltd. a semiconductor device IP company and Ngenics, Ltd an EDA tools development company. As a James Watt Professor in Electrical Engineering and Leader of the 30 members strong Glasgow Device Modelling Group (<http://web.eng.gla.ac.uk/groups/devmod/>) Asenov directs the development of 2D and 3D quantum mechanical, Monte Carlo and classical device simulators and their application in the design of advanced and novel CMOS devices. Asenov has more than 750 publications and more than 170 invited talks in the above areas. For more details please see: <http://scholar.google.co.uk/citations?user=owyRYmMAAAAJ&hl=en>.