



2024 IEDM Conference Theme
**Shaping Tomorrow's
Semiconductor Technology**



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2024 IEEE International Electron Devices Meeting

December 7-11, 2024

Hilton San Francisco Union Square
San Francisco, California

IEDM is pleased to announce increased
technical focus in the area of:

Memory Technology (MT)

Topics

Papers are solicited in the following themes of interest:

- Conventional memories
- Emerging memories
- 3D memory technologies
- Memories for AI and near-memory computing applications
- In-package memory for PPA augmentation

New or trending areas include:

- Memories to break the memory wall
- Memory-enabled artificial intelligence applications
- Memory-logic 3D stacking
- System-technology co-optimization
- Memory pooling and communication
- New memory hierarchy

Paper Submission

Submission deadline: July 11th

Single submission of final, four-page paper